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**\*BIBDATASHEET\*****CONFIRMATION NO. 4004**

Bib Data Sheet

SERIAL NUMBER 10/669,058	FILING DATE 09/24/2003  RULE	CLASS 438	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 65933-045
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APPLICANTS  
 Ryu Shimizu, Gifu, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\* <sup>P D</sup> NONE \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* <sup>P D</sup> YES \*\*\*\*\*  
 JAPAN 2002-287671 09/30/2002

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 09/15/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <u>Langdon</u> <sup>P D</sup> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 3
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TITLE  
 Method of manufacturing a semiconductor device including a multi-layer interconnect structure

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